L Number	Hits	Search Text	DB	Time stamp
77	1052	refresh adj controller	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/07/27 03:19
-	344	auto-refresh	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/07/27 03:19
-	205	auto-refresh and dynamic adj random adj access adj memory	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/06/26 17:36
-	104	(auto-refresh and dynamic adj random adj access adj memory) and (input adj buffer)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/06/26 17:38
-	2	((auto-refresh and dynamic adj random adj access adj memory) and (input adj buffer)) and bias adj circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/06/26 17:44
-	0	bias adj circuit adj second adj refresh adj signal	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/06/26 17:47
-	1	bias adj circuit SAME second adj refresh adj signal	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/06/26 17:49
-	0	low adj power adj precharge adj mode.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/06/26 17:50
-	10	low adj power adj precharge adjmode.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/06/26 17:50